

Test Your Dream !

AF8610E6

Debug - Station

Debug-Station is the equipment for the development and inspection of Test Program used in automatic Burn-In & Test System.
And this is used for the test of signal characteristics of devices.

* General Specification

- Object Device :
Memory Device (DRAM, SRAM, MaskROM, DDR)
- Address Size : 32 Ch
- Data Size : 72 I/O
- Board Size : 450mm× 570mm×1.6mm

* Timing Generator

- Cycle Rate : 100ns ~ 1.2ms (10ns step)
- Number of Clock : 10phases

* Pattern Generator

- Method : ALPG by micro program
- Number of Command Steps : 1024steps
- Generation Pattern : Address : 44 bits(X : 22bits, Y : 22bits)
Date : 36 bits



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